

May 17, 1978

Released GPD Lov

TMC0540ZA5503.

This is a British Gambling Crown
received from TI Nice, France
(Patrick Mc Lard).

Shirley Nystrom

March 28, 1978

Shipped GPD for TMC 549 -
Business Decisions (Crew)

Shirley Nystrom

2/13/78 shipped GPD for

TMC0540ZA5502 (McDonald -

Douglas "Harrier")

2/27/78 shipped GPD for TO351

first pass on chip

Shirley Nystrom

Need

1503 B



0271A shipped 4/12/78.

- > Second release of the controller chip for the "Speak 'n' Spell".
- First release was preliminary release with only 50% of algorithm coded.
- > The 0270 is one of three chips in the 'Speak 'n' Spell' and communicates with the 0280 (synthesizer chip). It has a 2K Rom and is similar to the earlier 260 chip.

Glen Thornton

TMC 1983 A

See Alex Release Memo

TM0262 (TI-5025)

- Shipped GPD on 3/21/78

TI-5025 is a handheld battery powered printing calculator with four functions, percent, and 4-key memory. The four functions feature chain operation.

Programmers: Tom Kish, Eric Evett

TM262 (TI-5025)

- Shipped GPD on 4/4/78

3?

On 2/29/78, product group requested the lengthening of double paper advance on the 262 so that last printed line would end up above plastic window. Modified algo to accomplish this. Since development was halted before rom reached mask shop, this did not constitute another pass

Programmers: Tom Kish, Eric Evett

TM262A (TI-5025)

- Shipped GPD on 5/1/78. Programmer: Eric Evett

TM0262 prototypes appeared to work properly except segments B, C, D, G turned on where blanks should be. This was due to use of "code 31" in output P1A. Code 31 must not be used in 1980 or 260 chip family. Revised algorithm to avoid code 31

TM984A (TI-33)

- Shipped GPO on 4/5/78

TI-33 is like the TI-30 with several additional features. It has 3 fully algebraic memories and an 8 digit mantissa key. This pass was made to fix algorithm error:

○ INV TAN locked up calculator.

Programmer: Eric Evett

TM 1993 (Little Professor Cost Reduction)

- Shipped GPD on 3/30/78

The Little Professor Cost Reduction differs from the Little Professor as follows:

Level switch replaced by level key.
On/Off switch replaced by integrated on/off.
Nullify - Power down feature also included.

Programmers: Donna Walker, Eric Evett

TP0321 (TI-35/50)

- Shipped GPD on 3/14/78.

TI-35/50 is an LCD scientific calculator. It has all the LED TI-30/ features except EE-roll. In addition, it has one-dimensional statistical functions, $X!$, 8 digit mantissa, 2 fully algebraic memories, and nullify-power down feature.

Programmer: Eric Evett

TP321A (TI-35/50)

- Shipped GPD on 5/4/78

Algorithm was revised to fix 2 errors:

1. $\cos(9 \times 10^{-99})$ gave 0 instead of error
2. σ_{n-1} had four levels of subroutine. This error was not detected by ALEX because simulator logic designed to catch subroutine overflow was not working properly.

Also revised display scan to meet display specifications. Also changed DAM from 15 to 7 to satisfy requirement that RAM $X-8$ be 0 when addressing digit latches.

This pass was also necessary to fix errors on the 320 chip.

Programmer: Eric Evett

TM0272 (SPELLING B)

- Shipped GPD on 4/25/78

SPELLING B is a handheld product with alphanumeric display. Designed to teach spelling to children 5 and up by using various spelling games. Also has SCRAMBLE game which may interest adults. The 272 talks with the 1984. The 1984 stores most of the 264 words.

TM1984 (SPELLING BEE)

- Shipped GPD on 4/25/78

T1984A (SPELLING BEE)

- Shipped GPD on 4/27/78

1984A was a revision of 1984 required to avoid using code 31. It does not count as a second pass because 1984 had not reached market shop.

Programmer: Debbie Thompson.

chip : 272B

calculator : Spelling B

shipped : 7~~6~~/7/78

change : calculator would not turn off when displaying RIGHT, WRONG

chip : 314 (New Release)

calculator : 1770

shipped : 7~~6~~/7/78

Description : Calculator has fully algebraic memory (1 memory).
Has functions $\frac{1}{x}$, x^2 , \sqrt{x} , π , $+/-$.

TM272A (SPELLING B)

Shipped GPD on 6/12/78.

Fixed error in the use of RETN instruction in 272 algorithm. The error was not found in the ALEX checkout because simulator did not simulate RETN instruction completely.

Programmer: Debbie Thompson

TM272B (SPELLING B)

Shipped GPD on

TM0985

- Skipped GPD on 6/9/78

The 985 is a TI-30-like algorithm done for TI-Japan. It has 2 levels of pending ops, chain operation, and X!

Programmer: Debbie Thompson

CHIP: TMS0255

REPLACES: TMS0251D

DATE RECEIVED IN HOUSTON: 3/20/78

PURPOSE: To change the characters and function prints of the 251D to a consumer oriented printer.

Changed from	to	Code
√	<	2A
π	>	2B
∩	⋮	32
X	#	37
!	!	3B
II	\$	3C
Δ	⊞	3D
π	+ ¹³	3E
Σ	⊞	3F

PRODUCT: TI-5230

LINDA FERRIO

CHIP: TMC0503

REPLACES: TMC0501E

DATE RECEIVED IN HOUSTON: 3/20/78

PURPOSE: To allow double key pushes to be seen by the 580 chip rather than screened out.

FROM

+DOUBLE KEY DETECTION = YES

TO

+DOUBLE KEY DETECTION = NO

PRODUCT: TI-5230

LINDA FERRIO

261 : TI 5040A Calculator

261A : 2-2-78 GPD date

Corrected two algorithm errors:

1. $\%T$ followed by $M+$ or $M-$ caused erroneous answer because ADD-ON-PCT flag was not reset initially by $M+$ or $M-$.
2. CM followed by P did not always result in blank audit character corresponding to P key push because CM did not clear location where audit character is saved.

January 17, 1978

Shipped GPD for TMC 548B (Security Analysis Crew)

Shirley Nyström

November 1, 1977

Shipped GPD for TMC 0548 - Security Analysis Com

Shirley Nystrom

Shipped 540X — October 20, 1977

This cron is for internal use only.

It contains the 3 programs on the 540Y which checks Mag. I. O. and the chips in the TIS8 & 59; plus, 8 additional programs to check the PC-100A and other related programs.

Shirley Nystrom

November 18, 1977

Reshipped GPD for Security Analysis Com (TMC548A)

Error in Program 9, "Warrant"

Shirley Nystrom

Debbie T.

TP0313

TI-1030

Shipped 3-1-78

modification of TMS923.

Features added : Square Root
Memory Indicator
Magnitude of Negative Numbers (-10^8+1)

Features Changed : Change Sign Key

TP0311

TI-1030

Shipped

Corrected error associated with live entry negative
8 digit mantissa.

1981 F	6/27/77	
1982 A	6/27/77	1982 was preliminary
1983	6/27/77	
1503	6/27/77	
1982 B	7/21/77	1F code was used by 1982H
540 Y	7/25/77	
1981 G	8/1/77	removed chip select to fix 0999 problem
Reynolds & Reynolds CROM	9/26/77	TMC 0540ZA5501
1503A	10/6/77	
1981 H	10/20/77	Fix edit off end of stack
540X	10/20/77	
548		
0261	10/25/77	
1981 H	was reshipped 11/17/77	Lauson Cook said we didn't ship Output PLA on 10/20/77

Shipped GPD for T0351A (4-12-78)

Shipped GPD for T0352 (4-13-78)

Generated PG tapes for both of the above

These 2 chips are the memory chips
for the product "Speak 'n Spell".

Each chip contains 16384 bytes.

Shirley Nystrom

-MSG 96735 -039 0528 CLDY TO RONR/ 71 03/19/79 05:14 PM

TO: BILL GREEN MOSA

CC: ~~ART HUNTER~~ RONR

K. BALA RONR

JOHN BARRETT RONR

TONY SOBOL CLDY

JIM MOORE CLDY

JIM CLARDY CLDY

FROM: VIVIAN DAVID CLDY

SUBJECT: 0262B

PLEASE ARRANGE THAT THIS REVISION GOES DIRECTLY TO HARD TOOLING AS WELL AS
PROTOTYPING.

WE BELIEVE THAT SINCE THE SOFTWARE CHANGE INVOLVED ONLY 2 INSTRUCTIONS THAT THE
RISK IS MINIMAL.

REGARDS,

VIVIAN DIVID

VED/SAM

5

1001 D same as last
940
970

	ORIG PLAN LATEST FCST	DATE	GPD RECD	SHIP PG	PROTO TOOL COMP	SLICE PROBE	SHIP 10 PROTOS	CUST OK	SHIP PROD TOOL	SHIP 100 PREPROD	SHIP INITIAL PROD	COMMENTS
1	501E					3/31	4/7	4/15	4/4	5/15	5/30	HPD (slip) prob.
2	526			(3/6)	3/21	4/10	4/17	5/1	5/5	5/30	6/30	"
3	564 A			(3/13)	3/24	4/16	4/23	5/5	5/1	5/27	6/26	
4	565 A			(3/13)	3/21	4/17	4/22	5/2	5/5	5/30	6/30	
5	566			(3/6)	3/21	4/14	4/21	5/2	4/30	5/30	6/25	
6	567 A			(5/13)	3/21	4/18	4/25	5/2	5/5	5/30	6/30	
7	568			(3/5)	3/18	4/10	4/17	5/1	5/2	5/29	6/27	
8	599					3/31	4/7	4/4	3/31	4/30	5/16	TIN Proto didn't work - New mlt started
9	596 B			(3/12)	3/18	4/3	4/3	4/11	4/25	5/23	6/23	New Op Amp (913700)
10	596 A											on Hold for B UVS.
11	253A					4/3	4/9	4/23	4/9	5/16	6/4	
12	254A					"	"	"	"	"	"	
13	251C					3/17	3/24	3/31	3/20	4/25	5/15	
14	561A					"	"	"	"	"	"	slices delayed
15	524					4/2	4/8	4/16				see 524A
16	524A			(3/5)	3/21	4/15	4/22	5/2	4/10	5/30	6/5	HPD Loading
17	562					3/31	4/7	4/16				see 562A
18	562 A			(3/13)	3/21	4/21	4/28	5/2	4/10	5/30	6/30	HPD Loading
19	563A					3/20	3/27	4/10	4/4	5/8	5/30	
20	595 B			(3/12)	3/18	4/3	4/11	4/25	4/28	5/23	6/23	
21	595 A											Hold
22	952										3/19	
23	1016		3/21					(5/2)				
24	1111		3/21	3/25	4/1	4/1	4/28	4/30	4/10	5/28	5/30	
25	531		2/19	(3/10)	3/20	4/10	4/17	5/1	5/2	5/29	6/30	
26	1014					(3/5)	3/17	3/19	2/14	3/21	4/1	
27	1214							3/19	3/6	3/21	4/1	
28	532 533		3/12	(3/14)	3/24	4/24	4/24	5/5	5/10	6/2	6/30	

OPERATING UNIT:

PREPARED BY: Jim Rountree

DATE: 3/19/75

MEMORANDUM

August 20, 1979

TO: K. Bala
Gordon Chilton
Vivian David
Jim Moore
Tony Sobol

FROM: Art Hunter
Don Mills
Alice Myers
Cyril Shepherd

SUBJECT: 5125 ALGORITHM CERTIFICATION

The 5125 algorithm checkout was completed 8/20/79 in accordance with CALD Standard Procedure 6.7 and ALEX testing requirements specified by the ALEX manager. The algorithm conforms to product requirements defined by the Printer/Display Products Division for 5125. ALEX test problems and results are available for review in Component Design.

Arthur C. Hunter
Art Hunter, Programmer

Don Mills 8/21/79
Don Mills, ALEX Manager

Alice Myers
Alice Myers, ALEX Designer

Cyril Shepherd
Cyril Shepherd, Product Mgr.

/lw

Custom Cams - Linda Ferris

Name	Chip #	Date
Black Type	EA5552	7/25/79
GE	EA5556	8/23/79
GMS III	EA5518	8/29/79
LANCE	EA5561	8/31/79
Survey	EA5563	8/31/79
LANCE - Reship	EA5561	9/6/79
M109A1	EA5568	9/10/79
Sound/Flash	EA5562	9/21/79
M110A2	EA5567	9/21/79
M101A1/M102	EA5566	9/24/79
Fmc	EA5544	9/25/79
M114A1	EA5564	10/4/79
M114A2	EA5565	10/4/79
SP. SITUATION	EA5560	10/17/79

Released GPD for TMC0540 ZA5551 on
June 21, 1979. Custom Crom for G.E.

Shirley Nyström

Released GPD & PG for Tm@355/CD 2601C on
June 5, 1979. English Rom for Mu. Challenger.
No data change; base set change (355B)

Shirley Nystrom

6/4/1979

Released 3 GPD'S for Home Computer
Groms, JMC 430.

1. CD 2033 - DIAG 21
2. CD 2034 - DIAG 22
3. CD 2011A - Number Magic

Skipper McDonnell

ALGORITHM DEVELOPMENT CHECKLIST

LCD Prog

- _____ Product Specification (Program Manager)
- _____ Algorithm Start (Programmer)
- _____ ALEX Test Procedure (ALEX Manager)
- _____ Algorithm Conforms to Spec (Programmer)
- _____ Test Team Assigned (Program Manager)
- _____ Algorithm Conforms to Spec (Test Team)
- _____ ALEX Checkout Complete (ALEX Manager)
- 9/21/78 Algorithm Certified for Release (ALEX Manager, Programmer, Program Manager)
- 9/21/78 EE GPD generated (Programmer)
- 9/21/78 EE GPD option card list checked (Programmer)
- 9/21/78 EE GPD PLA's verified (Programmer)
- 9/21/78 EE GPD transmitted (Programmer)
- 9/22/78 EE GPD verified (Programmer)
- 9/22/78 EE GPD Released (Programmer)
- _____ MIG SCAN (or equivalent) checklist complete
- _____ 5 Prototypes received (ALEX Manager)
- _____ Prototypes checkout complete (ALEX Manager)

COMMENTS:

ALGORITHM DEVELOPMENT CHECKLIST

TI 5025

- _____ Product Specification (Program Manager)
- 1/17/78 E.E. Algorithm Start (Programmer)
- _____ ALEX Test Procedure (ALEX Manager)
- 3/15/78 E.E. Algorithm Conforms to Spec (Programmer)
- _____ Test Team Assigned (Program Manager)
- _____ Algorithm Conforms to Spec (Test Team)
- _____ ALEX Checkout Complete (ALEX Manager)
- _____ Algorithm Certified for Release (ALEX Manager, Programmer, Program Manager)
- 5/1/78 E.E. GPD generated (Programmer)
- 5/1/78 E.E. GPD option card list checked (Programmer)
- 5/1/78 E.E. GPD PLA's verified (Programmer)
- 5/1/78 E.E. GPD transmitted (Programmer)
- 5/1/78 E.E. GPD verified (Programmer)
- 5/1/78 E.E. GPD Released (Programmer)
- _____ MIG SCAN (or equivalent) checklist complete
- _____ 5 Prototypes received (ALEX Manager)
- _____ Prototypes checkout complete (ALEX Manager)

COMMENTS:

ALGORITHM DEVELOPMENT CHECKLIST

Little Proj
CR

- _____ Product Specification (Program Manager)
- 2/15/78 E.E. Algorithm Start (Programmer)
- _____ ALEX Test Procedure (ALEX Manager)
- 3/22/78 E.E. Algorithm Conforms to Spec (Programmer)
- _____ Test Team Assigned (Program Manager)
- _____ Algorithm Conforms to Spec (Test Team)
- _____ ALEX Checkout Complete (ALEX Manager)
- _____ Algorithm Certified for Release (ALEX Manager, Programmer, Program Manager)
- 3/29/78 E.E. GPD generated (Programmer)
- 3/29/78 E.E. GPD option card list checked (Programmer)
- 3/29/78 E.E. GPD PLA's verified (Programmer)
- 3/29/78 E.E. GPD transmitted (Programmer)
- 3/29/78 E.E. GPD verified (Programmer)
- 3/30/78 E.E. GPD Released (Programmer)
- _____ MIG SCAN (or equivalent) checklist complete
- _____ 5 Prototypes received (ALEX Manager)
- _____ Prototypes checkout complete (ALEX Manager)

COMMENTS:

ALGORITHM DEVELOPMENT CHECKLIST

TISO40A

- _____ Product Specification (Program Manager)
- 6/15/77 E.E. Algorithm Start (Programmer)
- _____ ALEX Test Procedure (ALEX Manager)
- E.E. Algorithm Conforms to Spec (Programmer)
- _____ Test Team Assigned (Program Manager)
- _____ Algorithm Conforms to Spec (Test Team)
- _____ ALEX Checkout Complete (ALEX Manager)
- _____ Algorithm Certified for Release (ALEX Manager, Programmer, Program Manager)
- 2/1/78 E.E. GPD generated (Programmer)
- 2/1/78 E.E. GPD option card list checked (Programmer)
- 2/1/78 E.E. GPD PLA's verified (Programmer)
- 2/1/78 E.E. GPD transmitted (Programmer)
- 2/1/78 E.E. GPD verified (Programmer)
- 2/2/78 E.E. GPD Released (Programmer)
- _____ MIG SCAN (or equivalent) checklist complete
- _____ 5 Prototypes received (ALEX Manager)
- _____ Prototypes checkout complete (ALEX Manager)

COMMENTS:

ALGORITHM DEVELOPMENT CHECKLIST

BA II

- 1/24/78 Product Specification (Program Manager)
- 11/28/77 DC Algorithm Start (Programmer)
- _____ ALEX Test Procedure (ALEX Manager)
- 4/24/78 DC Algorithm Conforms to Spec (Programmer)
- _____ Test Team Assigned (Program Manager)
- _____ Algorithm Conforms to Spec (Test Team)
- _____ ALEX Checkout Complete (ALEX Manager)
- _____ Algorithm Certified for Release (ALEX Manager, Programmer, Program Manager)
- 7/25/78 DC GPD generated (Programmer)
- 7/25/78 DC GPD option card list checked (Programmer)
- 7/25/78 DC GPD PLA's verified (Programmer)
- 7/26/78 DC GPD transmitted (Programmer)
- 7/26/78 DC GPD verified (Programmer)
- _____ GPD Released (Programmer)
- _____ MIG SCAN (or equivalent) checklist complete
- _____ 5 Prototypes received (ALEX Manager)
- _____ Prototypes checkout complete (ALEX Manager)

COMMENTS:

ALGORITHM DEVELOPMENT CHECKLIST

TIS230

- 8/1/77 Product Specification (Program Manager)
- LF ● 8/1/77 Algorithm Start (Programmer)
- 5/23/78 ALEX Test Procedure (ALEX Manager)
- LF ● 4/28/78 Algorithm Conforms to Spec (Programmer)
- 5/23/78 Test Team Assigned (Program Manager)
- _____ Algorithm Conforms to Spec (Test Team)
- _____ ALEX Checkout Complete (ALEX Manager)
- _____ Algorithm Certified for Release (ALEX Manager, Programmer, Program Manager)
- _____ GPD generated (Programmer)
- _____ GPD option card list checked (Programmer)
- _____ GPD PLA's verified (Programmer)
- _____ GPD transmitted (Programmer)
- _____ GPD verified (Programmer)
- _____ GPD Released (Programmer)
- _____ MIG SCAN (or equivalent) checklist complete
- see below 5 Prototypes received (ALEX Manager)
- _____ Prototypes checkout complete (ALEX Manager)

see below

COMMENTS:

GPD dates

1376 6/8/78
 503 3/20/78
 LF 255 3/20/78
 587, 588, 572 6/9/78
 588A 6/23/78 (not used, bad checksum)
 588B 6/28/78

Proto Dates

Custom Crom

Chip # - ZA5548

Date - 5/24/79

Name - Real Estate Calculator, Inc

LINDA FERRIO

Chip # - ZA5547

Date - 6/4/79

Name - Sims

chip # - ZA5554

Date verified - 7/3/79

Name - SPRINGCREST

chip # - ZA5553

Date verified - 7/16/79

Name - Olympics

GPD releases by Shirley Nystrom

- 3-14-79 TMC0355 CD2601A (Mr. Challenger memory rom)
change in base set & data
- 4-2-79 TMC0430 CD2006A (Diagnostic Rom)
- 4-5-79 TMC0350 CD2305 (Super Stumpers 4, 5, 6)
- 4-9-79 TMC0430 CD2012A (Grammar, Part 1)
- 4-9-79 TMC0430 CD2013A (Grammar, Part 2)
- 4-9-79 TMC0430 CD2014A (Grammar, Part 3)
- 4-9-79 TMC0430 CD2011A (Number Magic)
- 4-10-79 TMC0430 CD2003C (Console, Part 1)
- 4-10-79 TMC0430 CD2004C (Console, Part 2)
- 4-10-79 TMC0430 CD2005C (Console, Part 3)
- 4-11-79 TMC0540 ZA5519 (Timkin)
- 4-11-79 TMC0540 ZA5541 (Poculator)
- 4-20-79 TMC0540 ZA5540 (Plenco)
- 4-23-79 TMC0285 CD2501C (Home Computer Synthesize Chip)
- 4-19-79 TMC0430 CD2032 (Speech Rom)
- 4-24-79 TMC0350 CD2325 (Speech Rom, Part 1)
- 4-25-79 TMC0350 CD2326 (Speech Rom, Part 2)
- 4-25-79 TMC0280 CD2801 (Xlater Synthesize Chip)
- 4-26-79 TMC0355 CD2601B (Mr. Challenger memory rom)
change in base set - no data change

GPD releases by Shirley Nystrom

5-2-79	TmC0430 CD 2020	(Chess, Part 1)
5-2-79	TmC0430 CD 2021	(Chess, Part 2)
5-2-79	TmC0430 CD 2022	(Chess, Part 3)
5-2-79	TmC0430 CD 2023	(Chess, Part 4)
5-2-79	TmC0540 ZA 5523	(A.F. Material)
5-3-79	TmC0430 CD 2017	(Home Financial Decisions, Part 1)
5-3-79	TmC0430 CD 2018	(Home Financial Decisions, Part 2)
5-4-79	TmC0430 CD 2032A	(Speech Gram)
5-4-79	TmC0540 ZA 5543	(Conelo PES Drilling)
5-4-79	TmC0350 CD 2311	(Br. Xlator, Part 1)
5-4-79	TmC0350 CD 2312	(Br. Xlator, Part 2)
5-4-79	TmC0350 CD 2313	(Br. Xlator, Part 3)
5-4-79	TmC0350 CD 2314	(Br. Xlator, Part 4)
5-10-79	TmC0430 CD 2026A	(Demo, Part 3)
5-11-79	TmC0350 CD 2315	(Sp. Xlator, Part 1)
5-24-79	TmC0350 CD 2307	(Super Stumper 7-8)

shipped T0215 5/1/79 - Translator
Phrase + Translate mode working
for CES
G

shipped T1501F 4/23/79 - TI 57
Debounce corrected after hardware simulator
frequency corrected.
G

shipped TP325A 7/10/79 Business CARD
Fixed double minus sign (with 7 dgt in display + mem $\neq \emptyset$,
hit +/-, \rightarrow - - 7777777).

shipped TAC0275B 7/23/79 Translator
Added REAC to PUC routine

Final releases of Home Computer Console - 0430 chip

CD 2003B - 3/6/79

CD 2004B - 3/8/79

CD 2005B - 3/8/79

GPD release for Pre-School, Part 1

TMC0430CD 2001A on 3-13-79

Release was due to name change from Pre-School to
Early Learning Fun

GPD release for Kinetic Art

TMC0430CD 2019 on 3-12-79

Shirley Nystrom

The following GPDs were reshipped but no revision change as the previous GPD was not released.

TMC0430CD2003B

TMC0430CD2004B

TMC0430CD2005B

} Home Computer
Console

3-6-79

TMC0540ZAS532 - Custom Crom for KODAK

3-7-79

Shirley Nyström

GPD Releases

- DEVICE -	- DATE -	- I. D. -
TmC0540ZA5529	1-29-79	Horizons II - Custom Crom
TmC0540ZA5530	1-29-79	Horizons II - Custom Crom
TmC0430CD2009A	1-30-79	Football, Part 1 - Grom
TmC0430CD2010A	1-30-79	Football, Part 2 - Grom
TmC0540ZA5531	1-31-79	Westinghouse - Custom Crom
TmC0540ZA5527	2-6-79	Surveyors Supply - Custom Crom
TmC0540ZA5532	2-12-79	Kodak - Custom Crom
TmC0540ZA5517	2-8-79	GMS# - Custom Crom
TmC0540ZA5508	2-13-79	Scotch Programs - Custom Crom
TmC0430CD2003B	2-20-79	Console, Part 1 - Grom
TmC0430CD2004B	2-20-79	Console, Part 2 - Grom
TmC0430CD2005B	2-20-79	Console, Part 3 - Grom
T285B CD2501B	2-20-79	Home Computer Synthesizer Chip
TmC0430CD2024	2-23-79	Demo, Part 1 - Grom
TmC0430CD2025	2-23-79	Demo, Part 2 - Grom
TmC0430CD2026	2-23-79	Demo, Part 3 - Grom
TmC0430CD2027	2-23-79	Demo, Part 4 - Grom
TmC0350CD2306	2-24-79	Home Computer Speech Rom
TmC0430CD2028	2-28-79	Pre School, Part 1 - Nice - Grom
TmC0430CD2029	2-28-79	Pre School, Part 2 - Nice - Grom

Shirley Neystrom

Chips: 588 E, CD2402 B, CD2403 A, CD2404 A

Calculator: TI 5230

Sent to Houston: 2/16/79

Verified: 2/19/79

Reasons: The dashes in the SETUP and RUN modes were cut down from 20 to 10 so the paper wouldn't stick.

The French CD2402B also had a wording change on
T (GT) NON-ZERO.

LINDA FERRIO

Released GPD for TMC0355CD2601 on 1-25-79
This is the memory row for Mr. Challenger.

Released 1st PG on T0355 on 1-25-79

Shirley Nystrom

Released GPD Jov TMC0540ZA5525 (Radrik) on 1-15-79
Custom Crew.

Released the following GPDs Jov TMC0430 (Groms)

CD2007	Exercise, Part 1	1-17-79
CD2008	Exercise, Part 2	1-17-79
CD2011	Number Magic	1-8-79
CD2012	Grammar, Part 1	1-16-79
CD2013	Grammar, Part 2	1-16-79
CD2014	Grammar, Part 3	1-16-79
CD2015	Home Finance II, Part 1	1-15-79
CD2016	Home Finance II, Part 2	1-16-79

Shirley Nystrom

Released GPD for TD281A on 12-7-78
Change in base set was reason for new revision letter.

Released GPD for TMC555A on 12-7-78
RPN - Retail Cron

Shirley Nystrom

Released Custom Cream (CONWAY) on 12-6-78

TMC 0540 2A55 24

Shirley Nystrom

Write Alex release memo for 321C
Remind David to issue 322 Alex memo.

272B ?

311 ?

Algo releases missing from Algo Cert. notebook:

272B, 311, 321C, BQD

Released GPD for TMC0551 (EE - retail cron) on 12-19-78

Shirley Nystrom

Released GPD for TMC0430 CD 2007 on 10-26-78
Grom - Exercise, Part 1

Released GPD for TMC0430 CD 2008 on 10-27-78
Grom - Exercise, Part 2

Released GPD for TMC0544 on 10-31-78
Agricultural Grom

Shirley Nystrom

- 10-26-78 = Released GPD for TMC0430 CD2007
Exercise, Part I
- 10-26-78 = Released GPD for TMC0430 CD2008
Exercise, Part II
- 10-31-78 = Released GPD for TMC0544
Retail Cram - Agricultural
- 11-16-78 = Released GPD for TMC0430 CD2002A
Pre School, Part 2
- 11-16-78 = Released GPD for TMC0430 CD2009
Football, Part 1
- 11-16-78 = Released GPD for TMC0430 CD2010
Football, Part 2
- 11-21-78 = Released GPD & PG for T0285 CD2501
- 11-22-78 = Released GPD for TMC0430 CD2003A
Console, Part 1
- 11-22-78 = Released GPD for TMC0430 CD2004A
Console, Part 2
- 11-22-78 = Released GPD for TMC0430 CD2005A
Console, Part 3
- 11-28-78 = Released GPD & PG tape for TMC 351C
Speak & Spell Resident Rem, Part 1
- 11-28-78 = Released GPD & PG tape for TMC 352B
Speak & Spell Resident Rem, Part 2
- 12-1-78 = Released GPD & PG tape for TMC0350 CD2302A
Vowel Power - S/N/S drop in rom
- 12-4-78 = Released GPD for TMC0555
Retail Cram - RPN

Shirley Nystrom

CALCULATOR: TI 5230

SHIPPED: 11/27/78

VERIFIED: 11/29/78

CHIP: CD2402A

REASON: FRENCH VERSION OF TMC588D

CHIP: CD2403

REASON: GERMAN VERSION OF TMC588D.

CHIP: CD2404

REASON: ITALIAN VERSION OF TMC588D

LINDA FERRIO

M E M O R A N D U M

November 14, 1978

TO: Rex Naden

FROM: Kaveri Gummattira
C. B. Wilson
Herman Harrison
Chuck Watkins
Gary Michael

SUBJECT: GPD, TMC 1985 (HHU Transmitter)

The Handheld Unit transmitter algorithm checkout is complete in accordance with P.C. Standard Procedures, 8/23/78. The algorithm conforms to the requirements of the Handheld Unit Product Specification.

Gk. Gummattira

Kaveri Gummattira

C. B. Wilson

C. B. Wilson, Hardware Manager

H. Harrison 11/15/78

Herman Harrison, Software Manager

Chuck Watkins

Chuck Watkins, Product Planning Manager

Gary Michael

Gary Michael, Product Planner

CHIP: 588D

SENT: 11/20/78

VERIFIED: 11/21/78

CALCULATOR: TI 5230

REASON: Change the sequence to get into the burn in since the 501 does not detect 2 keys down on the same digit time.

NEW SEQUENCE:

P on/off

number, #15

dec set switch

number (# of 30 sec groups bet prints)

Hold down TAPE ADV and SETUP

while pushing +

LINDA FERRIO

Released GPD for TMC0540ZA5516
on October 23, 1978. This is a custom
crom for Davis - Gilbert Co.

Released GPD for TMC0430CD2006
on October 24, 1978. This is the
diagnostic gram.

Shirley Nystrom

Chip: CD2400

Calculator: 54 CMOS

Resent: 9/21/78

Reverified: 9/25/78

Reason: Charles Curry couldn't get the first CD2400 sent to him to work in the pattern generator program, so he requested another copy.

L FERRIO

Chip: CD2400 (replaces TMC 582)
Calculator: 58 CMOS
Sent: 9/15/78
Verified: 9/18/78

Chip: CD2401 (replaces TMC 583)
Calculator: 58 CMOS
Sent: 9/18/78
Verified: 9/18/78

Chip: TMC 0573 (replaces TMC 571)
Calculator: 58 CMOS
Sent: 9/18/78
Verified: 9/18/78

Reason — Converted TI 58/59 code to non-volatile memory, fixed some of the errors on the TI 59, added DP 40 and tracing test branches, and saved the partition and ofix point information permanently.

LINDA FERRO

Released GPD for TMC0350CD2303 and TMC0350CD2304
on September 29, 1978. These were U.K. Speak & Spell
Resident Roms.

Released GPD for TMC0430CD2003, TMC0430CD2004, and
TMC0430CD2005 on October 7, 1978. These were Home
Computer Console Software.

Shirley Nystrom

Released GPD for TMC0430CD2001 on September 22, 1978
Crom - Pre School I

Released GPD for TMC0430CD2002 on September 22, 1978
Crom - Pre School II

Released GPD for TMC0540ZA5515 on September 22, 1978
Custom Crom - Government Marketing Services Gunnery

Shirley Nystrom

August 15, 1978

Released GPD for TMC0540ZA5513.

Custom crown for Horizon Tech.

Shirley Nystrom

August 14, 1978

Released GPD for TMC05402A5509.

Custom Cron for Data Lab.

Shirley Nystrom

(1) Released TMC0540ZA5514 (Custom Crem for White Motors Corp) on September 1, 1978

(2) Released TMC0350CD2302 (Vowel Rem for Speak & Spell) on September 8, 1978

Shirley Nystrom

Released GPD for TMC0540ZASS14 on September 1, 1978

Custom Crom for White Motors Corp.

Shirley Nystrom

AUGUST 28, 1978

CHIP: 587A

CALCULATOR: TI 5230

CHIP: 588C

CALCULATOR: TI 5230

REASON:

- (1) Correct program list when +, -, + (or +, *) appears in same column.
- (2) M+, M- do opposite operation to memory during edit row
- (3) If RM is used, no editing is allowed
- (4) Pending op, constant, N flag, % flag are all cleared at the beginning of a line.

L FERRIO

Debbie

(313)
Shipped 311A (1030) algorithm on
7/25/78.

Corrected an error using
negative number.

using 8 digit
 $3 + \text{---} 33333333 m+$
shows error and it
should not.

August 14, 1978

Released GPD for TMC0540ZA5511.

Custom cron for IMCO.

Shirley Nystrom

Chip: CD2402

Sent: 8/10/78 (verified 8/11/78)

Calculator: TI 5230

Reason: Contains the French wording for
the 5230. Replaces the 588 B and
must be used with the 587 and 572.

LINDA FERRIO

Aug. 8, 1978

Released GPD for TMC0350/2301 for SR-70 group.

○ This GPD contains the T0351B data.

Shirley Nystrom

July 21, 1978

Released GPD for custom cron TMC0540ZA5512,

Dr. Binkhorst.

Shirley Nystrom

July 11, 1978

Released GPD for TMC0540ZA5507

Custom cream → Schenk-Trebel

Shirley Nystrom

June 29, 1978

Released GPD for TMC0540ZA5505

Deutscher Lloyd, Lebensversicherung Custom Crew

Shirley Nystrom

Chip: TMC 588A

Calculator: TI-5230

Sent: 6/23/78

Reason: Make sure empty column after * 90
doesn't keep the add on 90 flag

No 588 or 588A chips were made

Chip: TMC 588B

Calculator: TI-5230

Sent: 6/28/78

Reason: When changing GPD for above fix,
I forgot to change the checksum.
Computer was down, so I was not able
to make a new GPD from the disk.

LINDA FERREO

1376

Sent to Houston 6/8/78

Display chip of TI 5230 Calculator

587, 588, 572

Sent to Houston 6/9/78

Algorithm chips for TI 5230 Calculator.

LINDA FERRIO

June 5, 1978

Released GPD for TIS9 custom cron TMC0540ZA5506 ^{FR. cron} (SOVAC).
GPD was sent to both Houston and Rieti, Italy

Shirley Neutron

T0271B released 5/25/78

→ Previous release - T0271A

→ Reason for T0271B release was to compensate for power supply problem during Power Up clear.

→ Compensation entailed a 20 ms delay from PUC to when R13 (chip select) is SET. (Pg 15);

Glen Thornton